

Electronic Acknowledgement Receipt

EFS ID:	3857441
Application Number:	10707797
International Application Number:	
Confirmation Number:	1796
Title of Invention:	Integrated Redundancy Architecture and Method for Providing Redundancy Allocation to an Embedded Memory System
First Named Inventor/Applicant Name:	Wayne F. Ellis
Customer Number:	21918
Filer:	Morgan Heller/Karen Jeffer
Filer Authorized By:	Morgan Heller
Attorney Docket Number:	BUR920030151US1
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$810
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	RCE.pdf	774253	no	3
			3137471abdc05cdd6356b809a07a90be09588ba7		
Warnings:					
Information:					
2		ResponsetoFinalOfficeAction.pdf	582594	yes	10
			883fa0e053a6f175dd20251c2f8d6eb57df615bf		
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment After Final		1	1	
	Claims		2	6	
	Applicant Arguments/Remarks Made in an Amendment		7	10	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	30315	no	2
			f870001f8b6fba96bafd074548fb7e9f8f02957f		
Warnings:					
Information:					
Total Files Size (in bytes):			1387162		
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